

## **Amendments to the Claims**

This listing of claims will replace all prior versions and listings of claims in the application:

### **Listing of Claims**

1. (Currently Amended) An image processing apparatus comprising:  
a reception unit adapted to receive at least three encoded image data;  
a decoding unit adapted to decode one of said the encoded image data to generate a main frame;  
a sub frame generation unit unit adapted to extract low frequency component from each one of the other encoded image data, and generate sub frames using a low frequency component the low frequency components extracted from each one of the other encoded image data; and  
an image signal generation unit adapted to generate an image signal including said the main frame and said the sub frames.
2. (Currently Amended) An apparatus according to claim 1, wherein said the reception unit receives said the at least three encoded image data through a serial bus.
3. (Currently Amended) An apparatus according to claim 2, wherein said the serial bus is based on the IEEE 1394-1995 standard.
4. (Currently Amended) An apparatus according to claim 1, wherein said the reception unit is a digital interface based on the IEEE 1394-1995 standard.
5. (Currently Amended) An apparatus according to claim 1, further comprising:  
a switch unit adapted to switch the encoded image data corresponding to said the main frame and the encoded image data corresponding to one of said the sub frames, in response to an operation of a predetermined operation key.

6. (Currently Amended) An apparatus according to claim 1, further comprising:  
a recording unit adapted to record the encoded image data corresponding to said the main  
frame on a storage medium, in response to an operation of a predetermined operation key.

7. (Currently Amended) An apparatus according to claim 1, wherein said the at least  
three encoded image data are based on the SD format of the DV standard.

8. An image processing method comprising steps of:  
receiving at least three encoded image data;  
decoding one of said the encoded image data to generate a main frame;  
extracting low frequency component from each one of the other encoded image data;  
generating sub frames using a low frequency component the low frequency components  
extracted from each one of the other encoded image data; and  
generating an image signal including said the main frame and said the sub frames.

9. (Currently Amended) A method according to claim 8, wherein said the reception step  
receives said the at least three encoded image data through a serial bus.

10. (Currently Amended) A method according to claim 9, wherein said the serial bus is  
based on the IEEE 1394-1995 standard.

11. (Currently Amended) A method according to claim 8, wherein said the plural-at least  
three encoded image data is received through a digital interface based on the IEEE 1394-1995  
standard.

12. (Currently Amended) A method according to claim 8, further comprising a step of:  
switching the encoded image data corresponding to said the main frame and the encoded  
image data corresponding to one of said the sub frames, in response to an operation of a  
predetermined operation key.

13. A method according to claim 8, further comprising a step of:

recording the encoded image data corresponding to said the main frame on a storage medium, in response to an operation of a predetermined operation key.

14. A method according to claim 8, wherein said the at least three encoded image data are based on the SD format of the DV standard.